

- Tentative Specification
- Preliminary Specification
- Approval Specification

## MODELNAME:YH043BD4003

## Version:MA01

|  |                  |
|--|------------------|
| <b>Customer: Common</b>  |                  |
| <b>APPROVED BY</b>   | <b>SIGNATURE</b> |
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| Note   |                  |
| _____  |                  |
| Please return 1 copy for your confirmation with your signature and comments. |                  |

| Approved By | Checked By | Prepared By |
|-------------|------------|-------------|
|             |            |             |

**Revision Record****修订记录**

| <b>Rev. No.</b> | <b>Date</b> | <b>Description<br/>描述</b>                    |
|-----------------|-------------|--|
| V01             | 2023-08-03  | Preliminary Specification Release.<br>初步规格发布 |
|                 |             |  |
|                 |             |  |
|                 |             |  |

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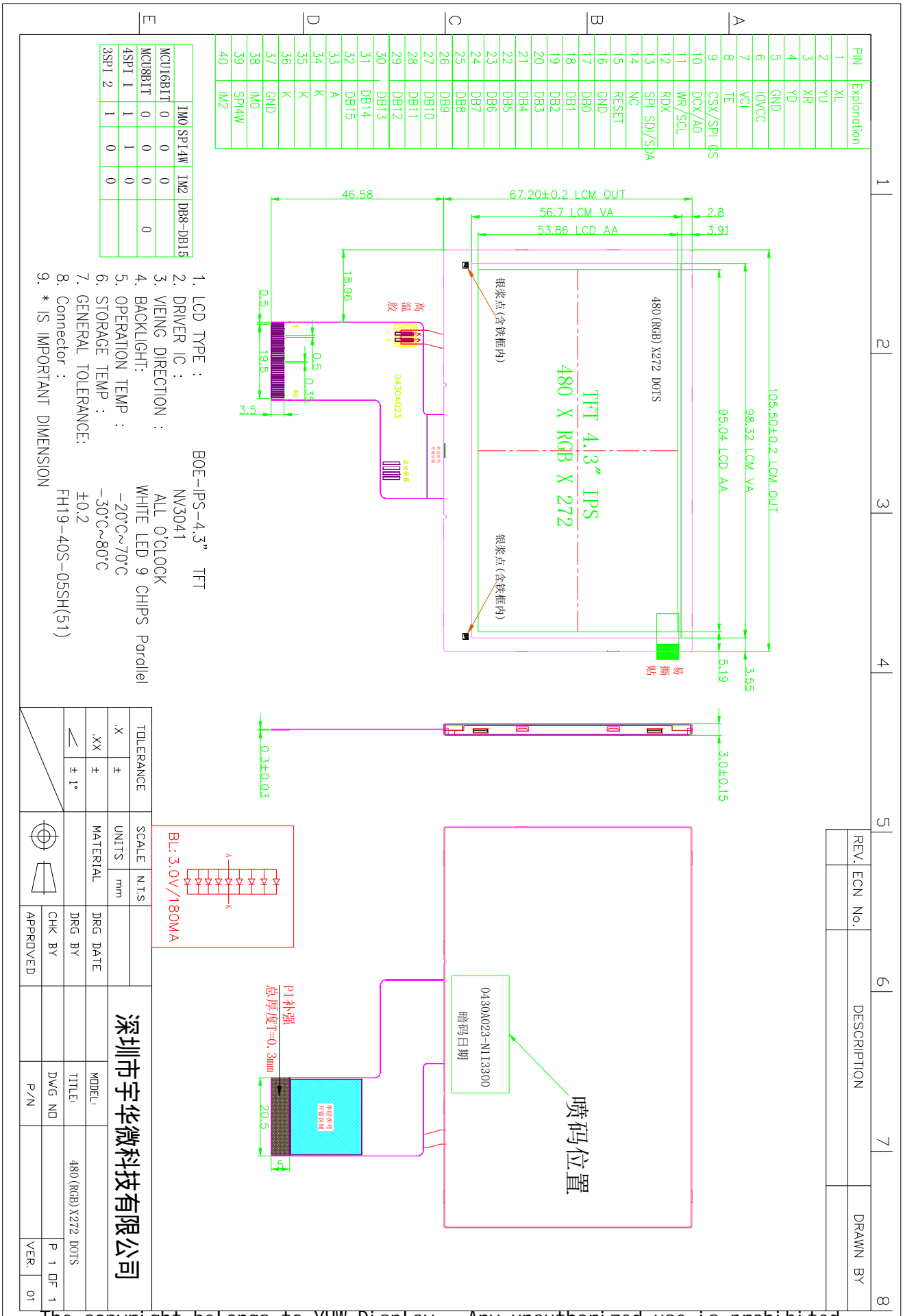
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## 1. General Specifications

| Item 项目                       | Contents 内容               | Unit 单位  |
|-------------------------------|---------------------------|----------|
| Size 尺寸                       | 4.3'TFT                   | inch 英尺  |
| LCM Dimension<br>LCM 尺寸       | 105.50(W)*67.20(H)*3.0(D) | mm       |
| LCD Active Area<br>LCD 显示区域   | 95.04*53.86               | mm       |
| Number of Dots<br>点数          | 480*RGB*272               | pixel 像素 |
| LCD Type<br>LCD 种类            | a-Si TFT                  |          |
| Viewing Direction<br>观看方向     | ALL O'CLOCK               |          |
| Driver IC<br>IC 驱动            | NV3041                    |          |
| Interface Type<br>接口类型        | MCU/SPI                   |          |
| Operating Temperature<br>运行温度 | -20°C~70°C                |          |
| Storage Temperature<br>储存温度   | -30°C~80°C                |          |
| Backlight Type<br>背光源类型       | 9 LEDs (9 灯并联)            |          |
| Weight<br>重量                  | TBD                       | g        |

2. Mechanical Drawing 机械图纸



### 3. Pin Assignments

引脚分配

| Pin No. | Symbol | Description   |
|---------|--------|---|
| 1       | XL     | TOUCH PLANE PIN   |
| 2       | YU     | TOUCH PLANE PIN   |
| 3       | XR     | TOUCH PLANE PIN   |
| 4       | YD     | TOUCH PLANE PIN   |
| 5       | GND    | Ground for digital circuits.  |
| 6       | IOVCC  | Power supply for logic interface  |
| 7       | VCI    | Power supply for digital interface  |
| 8       | TE     | Tearing effect signal is used to synchronize MCU to frame memory writing.   |
| 9       | CS     | Chip selection pin  |
| 10      | DCX/A0 | Display data/command selection pin in parallel interface. Display data/command selection pin in 4-line serial interface |
| 11      | WR/SCL | Write enable in MCU parallel interface..This pin is used to be serial interface clock.                                  |
| 12      | RD     | Read enable in 8080 MCU parallel interface.   |
| 13      | SDA    | The data is latched on the rising edge of the SCL signal.   |
| 14      | NC     | NC  |
| 15      | RESET  | This signal will reset the device and it must be applied to properly initialize the chip.                               |
| 16      | GND    | Ground for digital circuits.  |
| 17      | DB0    | MCU parallel interface data bus..   |
| 18      | DB1    | MCU parallel interface data bus..   |
| 19      | DB2    | MCU parallel interface data bus..   |
| 20      | DB3    | MCU parallel interface data bus..   |
| 21      | DB4    | MCU parallel interface data bus..   |
| 22      | DB5    | MCU parallel interface data bus..   |
| 23      | DB6    | MCU parallel interface data bus..   |
| 24      | DB7    | MCU parallel interface data bus..   |
| 25      | DB8    | MCU parallel interface data bus..   |
| 26      | DB9    | MCU parallel interface data bus..   |
| 27      | DB10   | MCU parallel interface data bus..   |
| 28      | DB11   | MCU parallel interface data bus..   |
| 29      | DB12   | MCU parallel interface data bus..   |
| 30      | DB13   | MCU parallel interface data bus..   |
| 31      | DB14   | MCU parallel interface data bus..   |
| 32      | DB15   | MCU parallel interface data bus..   |
| 33      | A      | LED BACKLIGHT(ANODE)  |
| 34      | K      | LED BACKLIGHT(CATHODE)  |
| 35      | K      | LED BACKLIGHT(CATHODE)  |
| 36      | K      | LED BACKLIGHT(CATHODE)  |
| 37      | GND    | Ground for digital circuits.  |
| 38      | IM0    | MCU interface mode select.  |
| 39      | SPI4W  | SPI interface mode select.  |
| 40      | IM2    | MCU interface mode select.  |

## 4. Electrical Specification

电气特性

### 4.1. DC Characteristics

DC 特性

| Item                                       | Symbol | Min | Typ. | Max | Unit |
|--|--------|-----|------|-----|------|
| TFT gate on voltage<br>TFT 栅极导通电压          | VGH    | --  | 16   | --  | V    |
| TFT gate off voltage<br>TFT 栅极不通电压         | VGL    | --  | -11  | --  | V    |
| TFT common electrode Voltage<br>TFT 公共电极电压 | Vcom   | --  | --   | --  | V    |

### 4.2. Typical Operation Conditions

平常运行条件

| Item                             | Symbol | Min.      | Typ. | Max.      | Unit |
|----------------------------------|--------|-----------|------|-----------|------|
| Analog Supply Voltage<br>模拟电源电压  | VCI    | --        | 3.3  | --        | V    |
| Digital Supply Voltage<br>数字电源电压 | VDD    | --        | 3.3  | --        | V    |
| I/O Supply Voltage<br>I/O 电源电压   | IOVCC  | --        | 1.8  | 3.3       | V    |
| Input High Voltage<br>输入高电压      | VIH    | 0.8*IOVCC | -    | IOVCC     | V    |
| Input Low Voltage<br>输入低电压       | VIL    | 0         | -    | 0.2*IOVCC | V    |
| Output High Voltage<br>输出高电压     | VOH    | 0.8*IOVCC | -    | -         | V    |
| Output Low Voltage<br>输出低电压      | VOL    | -         | -    | 0.2*IOVCC | V    |

### 4.3. Backlight Circuit Characteristics

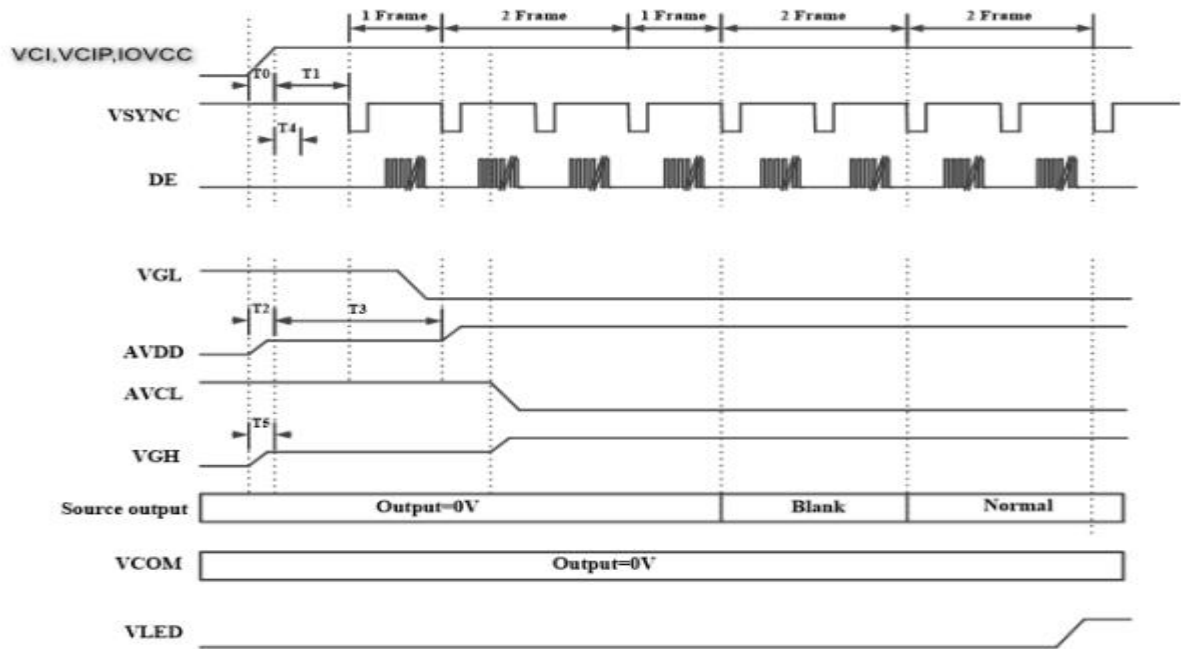
背光源线路特性

| Item 项目                  | Symbol 标志 | Min. | Typ. | Max. | Unit              |
|--------------------------|-----------|------|------|------|-------------------|
| LED Current 电流 LED       | IB        | --   | 180  | --   | mA                |
| LED Voltage 电压 LED       | Vf        | 2.8  | 3.0  | 3.3  | V                 |
| Brightness 亮度 of LCM LCM | L         |      | 300  |      | cd/m <sup>2</sup> |

### 4.4. Power on/off Sequence

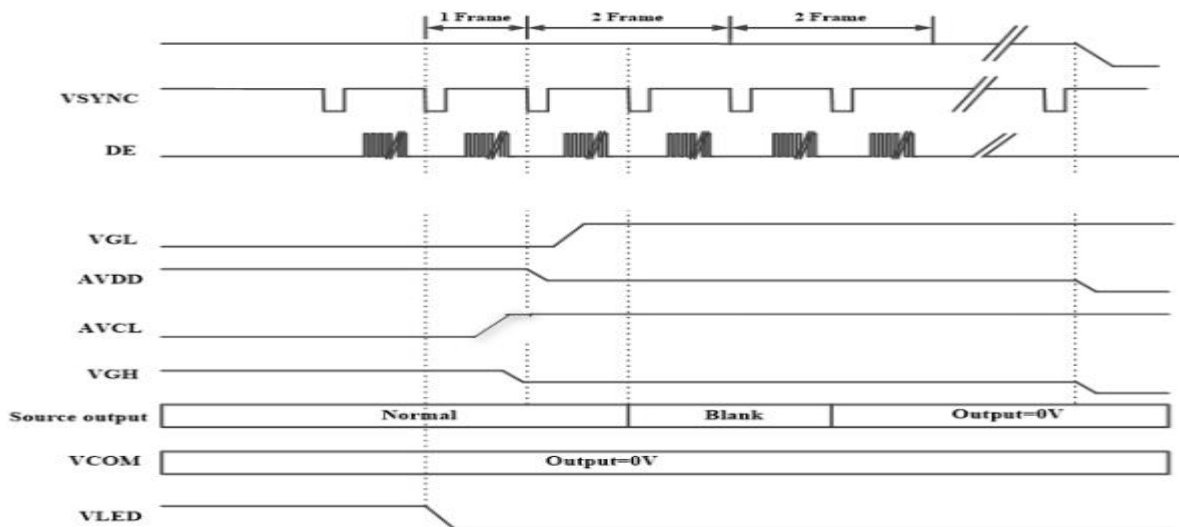
上下电顺序

#### Power On Sequence



|    | Description  | Min. Time        |
|----|--|------------------|
| T0 | Determined by the external power                           |                  |
| T1 | Time from stable VDD, VDDI, PVDD set-up to the first VSYNC | T1=0             |
| T2 | Time from AVDD=0V to AVDD=3.3V                             | T2=T0            |
| T3 | Time from AVDD=3.3V to AVDD=6.0V                           | T3=T1+ (1*Frame) |
| T4 | Time from stable VDD, VDDI, PVDD set-up to DISP asserted   | T4=0             |
| T5 | Time from VGH=0V to VGH=3.3V                               | T5=T0            |

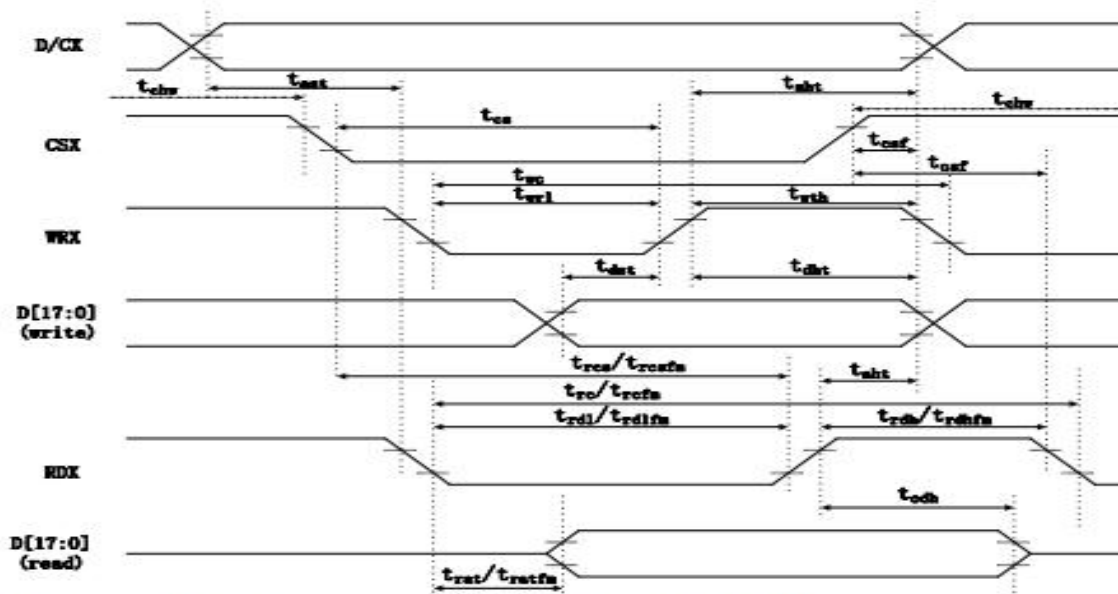
#### Power Off Sequence



### 4.5. Timing for MCU/SPI Interface

MCU/SPI 接口时序

Parallel MCU 16/9/8-bit BUS



Note: Logic high and low levels are specified as 30% and 70% of IOVCC for Input signals.

Table 16.3.1 AC characteristics of parallel MCU I/F in asynchronous mode

| Signal  | Symbol | Parameter                        | MIN | MAX | UNIT | Description                 |
|---------|--------|----------------------------------|-----|-----|------|-----------------------------|
| D/CX    | TAST   | Address Setup Time               | 0   |     | ns   |                             |
|         | THAT   | Address Hold Time (W/R)          | 10  |     | ns   |                             |
| CSX     | TCHW   | "S" "H" Pulse Width              | 0   |     | ns   |                             |
|         | TCS    | Chip Select Setup Time(W)        | 10  |     | ns   |                             |
|         | TRCS   | Chip Select Setup Time (Read ID) | 45  |     | ns   |                             |
|         | TRCSFM | Chip Select Setup Time (Read FM) | 355 |     | ns   |                             |
|         | TCSF   | Chip Select Wait Time (W/R)      | 10  |     | ns   |                             |
| WRX     | TWC    | Write Cycle                      | 66  |     | ns   |                             |
|         | TWRH   | Control Pulse H Duration         | 25  |     | ns   |                             |
|         | TWRL   | Control Pulse L Duration         | 25  |     | ns   |                             |
| RDX     | TRC    | Read Cycle(ID)                   | 160 |     | ns   | When Read ID                |
|         | TRDH   | Control Pulse H Duration(ID)     | 90  |     | ns   |                             |
|         | TRDL   | Control Pulse L Duration(ID)     | 45  |     | ns   |                             |
| RDX     | TRCFM  | Read Cycle(FM)                   | 450 |     | ns   | When Read From Frame Memory |
|         | TRDHFM | Control Pulse H Duration(FM)     | 90  |     | ns   |                             |
|         | TRDLFM | Control Pulse L Duration(FM)     | 355 |     | ns   |                             |
| D[17:0] | TDST   | Data Setup Time                  | 10  |     | ns   | CL,max=30pF<br>Cl,min=8pF   |
|         | TDHT   | Data Hold Time                   | 10  |     | ns   |                             |
|         | TRAT   | Read Access Time(ID)             |     | 40  | ns   |                             |
|         | TRATFM | Read Access Time(FM)             |     | 340 | ns   |                             |
|         | TODH   | Output Disable Time              | 20  | 80  | ns   |                             |

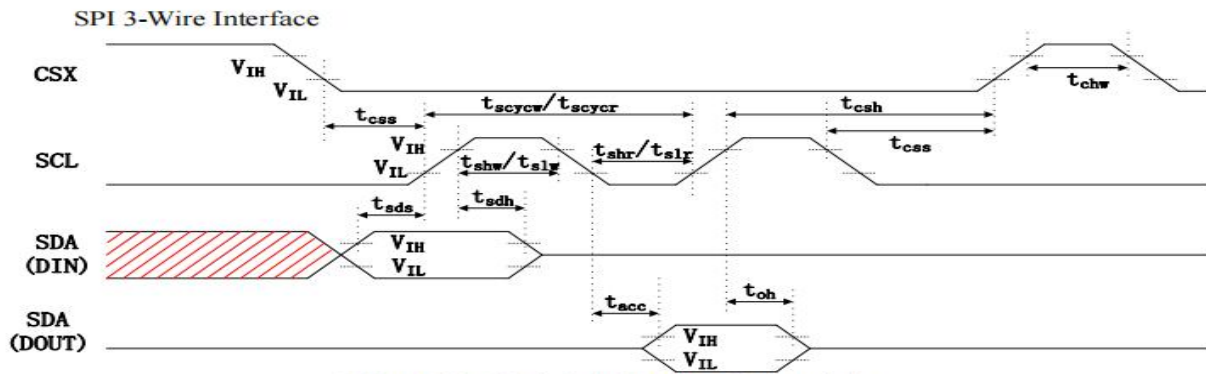


Table 17.3.2.1: 3-pin Serial Interface Characteristics

| Signal            | Symbol | Parameter                   | MIN | MAX | UNIT | Description             |
|-------------------|--------|-----------------------------|-----|-----|------|-------------------------|
| CSX               | TCSS   | Chip Select Setup Time      | 10  |     | ns   |                         |
|                   | TCSH   | Chip Select Hold Time       | 30  |     | ns   |                         |
|                   | TCHW   | Chip Select "H" Pulse Width | 30  |     | ns   |                         |
| SCL               | TSCYCW | Serial Clock Cycle(Write)   | 66  |     | ns   |                         |
|                   | TSHW   | S"L"H" Pulse Width(Write)   | 15  |     | ns   |                         |
|                   | TSLW   | S"L"L" Pulse Width(Write)   | 15  |     | ns   |                         |
|                   | TSCYCR | Serial Clock Cycle(Read)    | 150 |     | ns   |                         |
|                   |        | TSHR                        | 60  |     | ns   |                         |
|                   |        |                             |     |     |      | Width(Read)             |
|                   | TSLR   | S"L"L" Pulse Width(Read)    | 60  |     | ns   |                         |
| SDA(DIN) / (DOUT) | TSDS   | Data Setup Time             | 15  |     | ns   |                         |
|                   | TSDH   | Data Hold Time              | 5   |     | ns   |                         |
|                   | TACC   | Access Time                 | 5   | 50  | ns   | CLmax=30pF<br>CLmin=8pF |
|                   | TOH    | Output Disable Time         | 10  |     | ns   |                         |

Note 1: IOVCC=1.65 to 3.3V, VCI=2.6 to 3.3V, AGND=GND=0V. Ta=-30 to 70°C (to +85°C no damage)

Note 2: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. Logic high and low levels are specified as 10% and 90% of IOVCC for Input signals.

8.3.2.2 SPI 4-Wire Interface

Table 17.3.2.2: 4 pin Serial Interface Characteristics

| Signal          | Symbol | Parameter                   | MIN                      | MAX | UNIT | Description             |  |
|-----------------|--------|-----------------------------|--------------------------|-----|------|-------------------------|--|
| CSX             | TCSS   | Chip Select Setup Time      | 10                       |     | ns   |                         |  |
|                 | TCSH   | Chip Select Hold Time       | 30                       |     | ns   |                         |  |
|                 | TCHW   | Chip Select "H" Pulse Width | 30                       |     | ns   |                         |  |
| SCL             | TSCYCW | Serial Clock Cycle(Write)   | 66                       |     | ns   |                         |  |
|                 | TSHW   | S"L"H" Pulse Width(Write)   | 15                       |     | ns   |                         |  |
|                 | TSLW   | S"L"L" Pulse Width(Write)   | 15                       |     | ns   |                         |  |
|                 | TSCYCR | Serial Clock Cycle(Read)    | 150                      |     | ns   |                         |  |
|                 |        | TSHR                        | S"L"H" Pulse Width(Read) | 60  |      | ns                      |  |
|                 |        | TSLR                        | S"L"L" Pulse Width(Read) | 60  |      | ns                      |  |
| D/CX            | TDCS   | D/CX Setup Time             | 5                        |     | ns   |                         |  |
|                 | TDCH   | D/CX Hold Time              | 5                        |     | ns   |                         |  |
| SDA(DIN) (DOUT) | TSDS   | Data Setup Time             | 15                       |     | ns   |                         |  |
|                 | TSDH   | Data Hold Time              | 5                        |     | ns   |                         |  |
|                 | TACC   | Access Time                 | 5                        | 50  | ns   | CLmax=30pF<br>CLmin=8pF |  |
|                 | TOH    | Output Disable Time         | 10                       |     | ns   |                         |  |

Note 1: IOVCC=1.65 to 3.3V, VCI=2.6 to 3.3V, AGND=GND=0V. Ta=-30 to 70°C (to +85°C no damage)

Note 2: The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. Logic high and low levels are specified as 10% and 90% of IOVCC for Input signals.

## 4.6. Electro-Optical Characteristics

光电特性

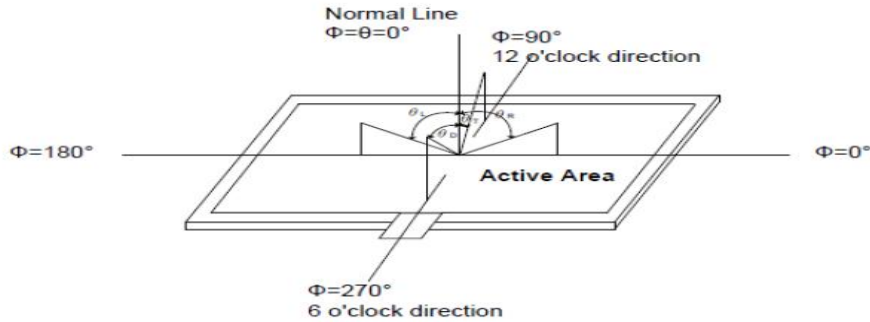
| Parameter             |            | Symbol        | Condition                      | Min.  | Typ.  | Max.  | Unit | Remark                            |
|-----------------------|------------|---------------|--------------------------------|-------|-------|-------|------|-----------------------------------|
| Viewing Angle Range   | Horizontal | $\Theta_3$    | CR > 10                        | 80    | 85    | -     | Deg. | Note 4.1                          |
|                       |            | $\Theta_9$    |                                | 80    | 85    | -     | Deg. |                                   |
|                       | Vertical   | $\Theta_{12}$ |                                | 80    | 85    | -     | Deg. |                                   |
|                       |            | $\Theta_6$    |                                | 80    | 85    | -     | Deg. |                                   |
| Contrast Ratio        |            | CR            | $\Theta = 0^\circ$             | 800   | 1000  | -     |      | C-light<br>(无APF)<br>Note 4.2/4.3 |
| Cell Transmittance    |            | Tr            |                                | 5.2   | 6.2   | -     | %    |                                   |
| Reproduction of color |            | Rx            | $\Theta = 0^\circ$             | 0.573 | 0.603 | 0.633 |      | @C Light                          |
|                       |            | Ry            |                                | 0.277 | 0.307 | 0.337 |      |                                   |
|                       |            | Gx            |                                | 0.284 | 0.314 | 0.344 |      |                                   |
|                       |            | Gy            |                                | 0.527 | 0.557 | 0.587 |      |                                   |
|                       |            | Bx            |                                | 0.115 | 0.145 | 0.175 |      |                                   |
|                       |            | By            |                                | 0.123 | 0.153 | 0.183 |      |                                   |
|                       |            | Wx            |                                | 0.275 | 0.305 | 0.335 |      |                                   |
|                       |            | Wy            |                                | 0.306 | 0.336 | 0.366 |      |                                   |
| Color Gamut           |            |               | $\Theta = 0^\circ$             | 40    | 50    | -     | %    |                                   |
| Response Time         |            | Tr+Tf         | Ta= 25°C<br>$\Theta = 0^\circ$ | -     | 30    | 35    | ms   | Note 4.5                          |

## 5. Measurement System

测试系统

### 5.1. LCM Viewing Angle

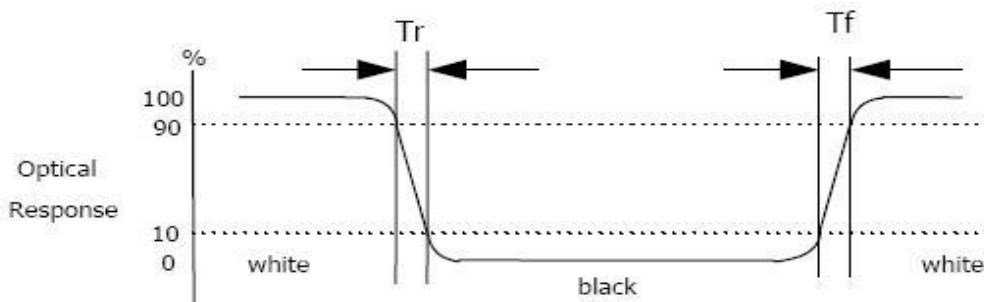
LCM 观看角度



Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. 观看角度是对比度大于 10 的角度。角度是针对水平(x 轴)和垂直(y 轴)相对于与 LCD 表面垂直的 z 轴确定的。

### 5.2. Response Time

响应时间



Response time is the time required for the display to transition from white to black (Rising time,  $T_r$ ) and from black to white (Falling time,  $T_f$ ) for additional information. 响应时间是显示器从白色过渡到黑色（上升时间， $T_r$ ）和从黑色过渡到白色（下降时间， $T_f$ ）以获取附加信息所需的时间。

### 5.3. Contrast Ratio (CR)

对比度

Contrast Ratio (CR) is defined mathematically as:

对比度 (CR) 在数学上定义为:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

对比度 =  $\frac{\text{全白像素的表面亮度}}{\text{全黑像素的表面亮度}}$

Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white.

表面亮度是整个 LCD 表面的中心点，距离表面 500 毫米，所有像素都显示白色。

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## 6. Reliability Test Items

可靠性测试项目

| Test Item 测试项目                       | Test Condition 测试状态   | Test result determinant gist<br>测试结果决定性要点  |
|--------------------------------------|---|--|
| High temperature storage 高温<br>储存    | 80±3°C,48H;   | 1. Inspection after<br>2~4hours storage at room<br>temperature, the sample<br>shall be free from defects:<br>a). Air bubble in the LCD;<br>b). Non-display;<br>c). Glass crack;<br>2. The electrical<br>characteristics requirements<br>shall be satisfied.<br>1. 样品在室温下存放 2~4<br>小时后检验, 样品应无缺<br>陷:<br>a) LCD 中有气泡;<br>b)不显示;<br>C) 玻璃裂纹;<br>2、满足电气特性要求 |
| Low temperature storage 低温<br>储存     | -30±3°C,48H;  |  |
| High temperature operation 高<br>温运行  | 70±3°C,48H;   |  |
| Low temperature<br>operation 低温运行    | -20±3°C,48H;  |  |
| High temperature<br>/ humidity 高温/亮度 | 60°C±3°C,90%±3%RH,48H;  |  |
| Thermal Shock<br>热冲击                 | -20°C/0.5h~+70°C/0.5h<br>for a total 24 cycles; 24 小时总循环  |  |
| Vibration Test<br>震动测试               | Frequency:10Hz~55Hz~10Hz;<br>Amplitude:1.5mm, X, Y, Z<br>direction for total 1H;<br>(Packing condition) |  |
| ESD test<br>ESD 测试                   | ±4KV, Human Body Mode,150pF/330Ω;<br>±8KV, Air Mode, 150pF/330Ω;  |  |

### Remark:

#### 备注

1.The test samples should be applied to only one test item.

测试样品只能应用于一个测试项目。

2.Sample size for each test item is 2pcs.

每个测试项目的样本大小为 2 个。

3.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical

Characteristic, Optical Characteristic.

3.故障判断标准：基本规格、电气特性、机械特性、光学特性。

## 7. Suggestions for Using LCD Modules

### LCD 模块的使用建议

1) The display panel is made of glass and polarizer. As glass is fragile. It tends chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

显示面板由玻璃和偏光片制成。因为玻璃易碎，在处理过程中，特别是在边缘处，它会比较容易碎裂。请避免掉落或震动。请勿使其跌落或撞击而受到机械冲击。

2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

如果显示面板损坏和液晶物质泄漏，请务必不要让其进入您的嘴里。如果该物质接触到您的皮肤或衣服，请用肥皂和水清洗。

3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

请勿对显示器表面或相邻区域施加过大的力，因为这可能会导致色调发生变化。请勿赤手触摸显示屏。这会弄脏显示区域。

4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Please handle this polarizer carefully. Please do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Please do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will be damaged, or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.

覆盖在液晶模组显示面上的偏光片柔软，且容易划伤。请小心处理这个偏光片。请不要用比HB 铅笔芯（玻璃、镊子等）更硬的任何东西接触、推动或摩擦暴露的偏光片。请勿在展示区域放置或粘贴任何物品，以免在其上留下痕迹。其表面冷凝和因寒冷而与端子接触会损坏或弄脏偏光片。产品在低温下测试后，必须在容器中预热，然后再与室温空气接触。

5) If the display surface becomes contaminated, exhale on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents

- Isopropyl alcohol
- Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

如果显示器表面被污染，请在表面呼气并用柔软的无尘布轻轻擦拭。如果它被严重污染，请用以下溶剂之一弄湿布

- 无水丙醇

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- 乙醇

请勿用力擦洗，以免损坏显示器表面。

6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water

- Ketone

- Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

6) 上述以外的溶剂可能会损坏偏光片。请勿使用以下溶剂。

- 水

- 丙酮

立即擦掉唾液或水滴，长时间与水接触可能会导致变形或褪色。避免接触油和脂肪。

7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

小心操作以尽量减少电极的腐蚀。水滴、湿气凝结或电流会加速电极的腐蚀。

8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I O cable or the backlight cable.

使用安装孔安装 LCD 模块。安装 LCD 模块时，请确保其没有扭曲、翘曲和变形。尤其不要强行拉扯或弯曲 I O 电缆或背光电缆。

9) Do not attempt to disassemble or process the LCD module.

请勿尝试拆卸或加工 LCD 模块。

10) NC terminal should be open. Do not connect anything.

NC 终端应打开。不要连接任何东西。

11) If the logic circuit power is off, do not apply the input signals.

如果逻辑电路电源关闭，请勿施加输入信号。

12) Electro-Static Discharge Control, since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before removing LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded. Make certain

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the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

静电放电控制，由于该模块使用 CMOS LSI，因此应与普通 CMOS IC 一样注意静电放电。为防止静电损坏元件，请注意保持最佳工作环境。

- 从包装箱中取出 LCM 或将其装入套件之前，请确保

模块和您的身体具有相同的电势。处理 LCD 模块时一定要把身体接地。

- 组装所需的工具，例如烙铁，必须正确接地。确保烙铁的交流电源不漏电。使用电动螺丝刀安装 LCM 时，螺丝刀应具有接地电位，以尽可能减少来自电机换向器产生的火花的电磁波传输。

- 为减少静电产生量，请勿在干燥条件下进行组装和其他工作。为减少静电的产生，请注意工作中的空气不要太干燥。建议相对湿度为 50%-60%。尽量使您的工作服和工作台的电位为地电位。

- LCD 模块涂有一层薄膜以保护显示表面。剥离此保护膜时请小心，因为可能会产生静电。

13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.

- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.

- Do not damage or modify the pattern writing on the printed circuit board.

- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.

- Do not drop, bend or twist the LCM.

由于 LCM 已高精度组装和调整，请避免对模块施加过大的冲击或对其进行任何改动或修改。

- 请勿改动、修改或改变金属框架上拉环的形状。

- 不要在印刷电路板上打额外的孔，修改其形状或改变要连接的组件的位置。

- 不要损坏或修改印刷电路板上的图案书写。

- 绝对不要改装斑马橡胶条（导电橡胶）或热封接头。

- 除焊接接口外，请勿使用烙铁进行任何改动或修改。
- 不要掉落、弯曲或扭曲 LCM。

## 8. Storage Method

### 储存方法

1) Store in an ambient temperature of  $23^{\circ}\text{C}\pm 5^{\circ}\text{C}$ , and in a relative humidity of  $55\%\pm 15\%$ . Do not exceed 12 months and expose to sunlight or fluorescent light.

环境温度为  $23^{\circ}\text{C}\pm 5^{\circ}\text{C}$ ，相对湿度为  $55\%\pm 15\%$ 。不要超过 12 个月并暴露在阳光或荧光灯下。

2) Store in a clean environment, free from dust, active gas, and solvent.

存放在清洁的环境中，无灰尘、无活性气体和溶剂。

3) LCM module is stored in warehouse, Store in antistatic container, there may be air bubble, between the protective film on the surface of polarizer and polarizer. It can only be found under LE light, before production, IQC should be inspected directly by LED light, if residual traces are found alcohol should be used to wipe them.

LCM 模块存放在仓库中，偏光片和偏光片表面的保护膜之间可能会有气泡。只能在 LED 灯下发现，生产前，IQC 应直接用 LED 灯检查，如有残留痕迹，可用酒精擦拭。